



# COPY

XA-9594

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

MATSUOKA, Hideyuki et al.

Intl. Appln. No.: PCT/JP00/03723

Intl. Filing Date: 8 June 2000

For: SEMICONDUCTOR MEMORY DEVICE AND MANUFACTURING METHOD  
THEREOF

\* \* \*

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Prior to examination, please amend the above-identified  
patent application as indicated below.

IN THE CLAIMS:

1. (Amended) A semiconductor memory device comprising:  
a semiconductor substrate;

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